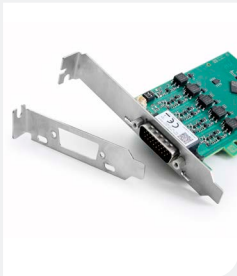




Learn more about  
this product



## Kvaser PCIe 4xLIN four-channel interface

The Kvaser PCIe 4xLIN is a four-channel LIN interface engineered for stable and predictable performance in professional test and development environments. Each channel is individually galvanically isolated, providing robust electrical separation and reliable operation in multi-network setups.

With an FPGA-based LIN implementation and 1  $\mu$ s timestamp resolution, the card delivers deterministic timing suitable for detailed analysis and synchronized measurements. Its low-profile PCI Express design integrates easily into desktop and industrial PCs.

Support for software-based time synchronization (PHC) enables accurate alignment across multiple devices. Fully compatible with Kvaser LINlib and CANlib, the PCIe 4xLIN offers a consistent and dependable platform for advanced LIN communication tasks.



### Warranty

2-Year warranty. See our general conditions and policies for details.



### Support

Free support for all products by contacting [support@kvaser.com](mailto:support@kvaser.com)



### EAN

73-30130-01802-0

## Major Features

- PCI Express LIN interface.
- Supports LIN, up to 20 kbit/s.
- Four fully featured, time-synchronized, individually galvanically isolated LIN channels utilizing Kvaser LIN-IP implemented in FPGA.
- Quick and easy plug-and-play installation.
- 100% compatible with applications written for other Kvaser LIN hardware with Kvaser LINlib.
- Supports simultaneous usage of multiple Kvaser interfaces.
- Kvaser software-based TimeSync – time synchronization with a host-referenced clock source.
- Low-profile PCI Express card.
- A longer and shorter bracket are included to fit different computer cases.

## Support

Documentation, Kvaser SDK and drivers can be downloaded for free at [www.kvaser.com/downloads](http://www.kvaser.com/downloads).

Kvaser SDK is a free resource that includes everything you need to develop software for the Kvaser CAN interfaces. Includes full documentation and many program samples, written in C, C++, C#, Delphi, Visual Basic, Python and t script language.

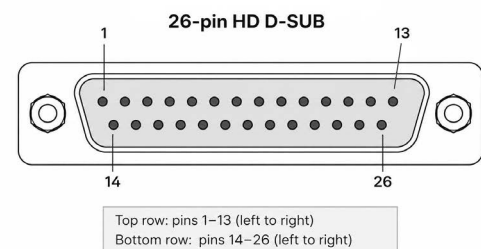
Kvaser CAN hardware is built around the same common software API. Applications developed using one device type will run without modification on other device types.



## Technical Data

|                           |                             |
|---------------------------|-----------------------------|
| Dimensions                | 68 x 100 mm                 |
| Galvanic isolation        | Yes                         |
| Host interface            | PCI Express                 |
| LIN Bit Rate              | Up to 20 kbit/s             |
| LIN Channels              | 4                           |
| LIN Connector             | 26-pin HD D-SUB             |
| LIN Controller            | Kvaser LIN IP in FPGA       |
| LIN Transceivers          | TLIN2027-Q1                 |
| Operating Systems         | Linux, Windows <sup>1</sup> |
| Power consumption         | Typically 3W                |
| Regulatory Compliance     | CE, FCC                     |
| Relative humidity         | 0% to 85% (non-condensing)  |
| Temperature Range         | -40 to +85 °C               |
| Timestamp Resolution      | 1 µs                        |
| Weight with long bracket  | 56 g                        |
| Weight with short bracket | 50 g                        |

<sup>1</sup> Windows 10 (IA-32 and x86-64)  
Windows 11 (x86-64)



| Function         | Pins           | Notes                                 |
|------------------|----------------|---------------------------------------|
| LIN Channels 1–4 | 11, 12, 13, 14 | One LIN signal per channel            |
| VREF 1–4         | 10, 6, 17, 24  | Required for Commander mode operation |
| Ground (GND) 1–4 | 19, 20, 21, 22 | Separate grounds per channel          |
| Shield / Chassis | Shell          | Cable shield connection               |
| Reserved Pins    | All others     | Not connected or reserved             |